

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Douglas T. Grider
Serial No: 09/967,044
Examiner: Fetsum Abraham
Filed: 09/28/2001
For: METHOD FOR TRANSISTOR GATE DIELECTRIC LAYER WITH UNIFORM NITROGEN CONCENTRATION

Docket No: TI-31118
Conf. No: 4815
Art Unit: 2826

2826

#4

Elective
10/4/02
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ELECTION

Assistant Commissioner for Patents
Washington, DC 20231

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231 on 9-19-02.

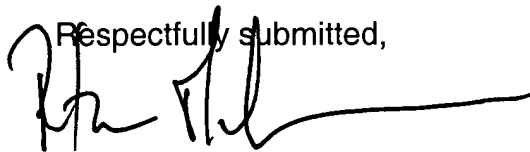

Ann Trent

Dear Sir:

This election is offered in response to the Examiner's restriction requirement mailed August 30, 2002.

Applicant hereby elects to pursue Group II of Claims 1-10, drawn to process of making a device, without traversing the Examiner's restriction requirement.

Respectfully submitted,



Peter K. McLarty
Agent for Applicant
Reg. No. 44,923

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